REMARKS / ARGUMENTS

Examiner Mengistu is thanked for the thorough examination of the subject Patent Application. The claims have been carefully reviewed and amended, and are considered to be in condition for allowance.

This invention provides a method and apparatus for displaying an unscaled image frame on an LCD panel. The method and apparatus uses the same line buffers available to the digital signal processor DSP formerly used for scaling the displayed image up or down in size. No extra frame buffers are required by this invention since the frame rates of the source image and the LCD panel are the same. The image frame buffer is written to the LCD panel on every other panel vertical synchronization pulse. The vertical synchronization timing is shifted to the left or right in the time domain to center the image on the LCD panel.

We added one label to figure 3 and deleted one label from figure 3. This was done to make the specification consistent with the figures.

Two paragraphs were replaced in order to correct spelling mistakes.

Reconsideration of the objections to the drawings, to the specification, and to claims 1-18 due to informalities and under 35 U.S.C. 112, first paragraph, as failing to comply with the enablement requirement, is requested based on the following.

A paragraph was added to the detailed description part of the specification to provide a description of a circuit block diagram which was added in a new figure 8 sheet. In addition, this new paragraph describes several terms which were mentioned in the claims but were not previously described in the specification.

The examiner has said in the office action that the specification has failed to teach one skilled in the art how to display an image by skipping the odd image and how one can have good quality image information by only an even line image. The following existing excerpt from line 15 page 7, of the existing specification provides the answer to the examiner's concern.

"The odd image lines are blanked or not displayed 430. This method requires that one frame of data, the odd frames, is skipped every other frame. Since the odd source lines are directly adjacent to the even source lines, eliminating the odd scan lines results in minimal loss of information."

Reconsideration of the examiner's objections to Claims 1-18 because of informalities is requested based on the following.
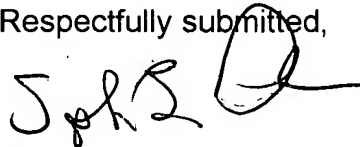
Claims 1, 5, 11 and 15 have been amended by replacing "Vsync-pl" with

"Vertical synchronization pulse" as recommended by the examiner. Also, the specification

made this change. In addition, Claims 2, 6, 11, 13, and 16 have been amended by

replacing "Vsync" with "Vertical synchronization" as recommended by the examiner.

The examiner is thanked for the thorough review of this patent application. The

changes to the specification do not introduce any new matter.

It is requested that should there be any problems with this Amendment, please call

the undersigned Attorney at (845) 452-5863.

Respectfully submitted,

Stephen B. Ackerman, Reg. No, 37,761

Attachments

Amendments to the Drawings:

The attached sheets of drawings includes changes to Fig.3 and a new Fig.8 sheet. The replacement Fig.3 sheet replaces the previous Fig.3 sheet. In addition, the new sheet that has been added is labeled Fig.8.

Attachments:　　　　Replacement Sheet

　　　　　　　　　　Annotated Sheet Showing Changes

　　　　　　　　　　New Sheet